

**AUTOMATIC GAIN CONTROL BASED ON MULTIPLE INPUT REFERENCES IN A
VIDEO DECODER**

* * * * *

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] Not applicable.

**STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR
DEVELOPMENT**

[0002] Not applicable.

BACKGROUND OF THE INVENTION

[0003] This invention is in the field of digital video systems, and is more specifically directed to video decoders.

[0004] Modern high-performance video and television displays, especially
5 plasma and liquid-crystal (LCD) displays, are adapted to receiving digital signals
corresponding to the information to be displayed. These digital inputs indicate the
intensity, typically by component, to be displayed by each picture element ("pixel") of
the display. For example, modern component video signals include a component value
for each of the pixel attributes of luma ("Y"), chroma-blue ("Pb"), and chroma-red
10 ("Pr"). As a result, modern high resolution displays, having over one thousand pixels in
each dimension, with each pixel responsive to as much as a twenty-four bit digital
signal, are able to render high fidelity images at real time data rates.

[0005] As known in the art, video inputs are communicated and stored in a wide
variety of formats. Broadcast television signals are still communicated in the analog
15 domain, and these analog signals are communicated according to multiple standards
around the world. In addition, video signals from other sources are now also available

for display on digital displays. These other sources include cable and satellite digital video transmissions, video cameras, and video playback devices such as DVD players and video cassette recorders. In any case, these signals are often in the form of "composite" video signals, in that the color signals are communicated in the form of "luma" and "chroma" (or color difference) signals, rather than as intensity levels for each of the primary component colors. These signals are also typically analog signals. Examples of the applicable standards for conventional video signals include the well-known NTSC (National Television Systems Committee), PAL, and SECAM composite video signal standards.

10 [0006] Video decoder functions are now commonly used in many high-performance digital display and television systems for receiving video signals from these various sources and converting the video signals into a digital form for display. For example, a so-called "set-top box" for receiving cable or satellite digital video transmissions and for driving a digital video display typically includes a video decoder
15 function. Modern set-top boxes also often have auxiliary inputs for receiving video signals from other sources, from which the video decoder in the set-top box generates the digital video output signals. Other systems that include a video decoder function include video decoder cards for personal computers, personal video recorders (PVRs) for digitally recording broadcast, cable, or satellite transmissions for later viewing,
20 digital video projectors, digital VCRs and DVD recorders, video or home theater receivers, and indeed digital television sets and computer displays that are themselves (i.e., without an external set-top box) capable of digitally displaying video output from conventional analog input signals.

[0007] Recently, the video decoder function in these systems has been
25 implemented within a single integrated circuit. Examples of conventional video decoder integrated circuits include the TVP5145 digital video decoder available from Texas Instruments Incorporated, and the SAA7115 video decoder available from Philips Semiconductors.

[0008] In the decoding of input signals into digitally displayable output signals, conventional video decoders typically apply gain to the decoded signal, so that the digital output signals have amplitudes that fit well within the dynamic range of the display device. Conventional video decoders apply this gain by way of automatic gain control (AGC), which in its general sense amplifies a varying input voltage using a gain that depends on the input voltage itself. AGC circuits and functions thus automatically control the amplifier gain so that the output voltage remains constant or within a predetermined dynamic output range.

[0009] In conventional video decoders for converting analog video signals into digital display signals, the AGC function measures the amplitude of the analog signal at a known time within the periodic signal, and adjusts the gain of its output signal based on that amplitude. Typically, conventional video decoders sample the analog video signal at its synchronization ("sync") level, which is a portion of the analog signal in the horizontal blanking interval that is not displayable by the display but which is used to synchronize the displayable portions of the signal with the display scan lines. The sync "height", or amplitude (i.e., the difference between the sync level and a reference level, such as the "back porch" level), is used in these conventional video decoders as the control input to an AGC function, in response to which the AGC gain of the video decoder is set.

[0010] It has been observed, in connection with this invention, that the sync amplitude may not be representative of the actual amplitude of the video information to be displayed. For example, in the well-known NTSC standard, the nominal sync amplitude is -40 IRE, but this sync pulse is frequently compressed or clipped. If this clipping occurs, the clipped or compressed sync pulse received by the digital video decoder will be at a lower amplitude than its ideal amplitude. The AGC function in the conventional video decoder will, as a result, set its gain undesirably high in an attempt to compensate for the lower amplitude sync height, but this gain will be too high for the video signal itself (which was not clipped). The resulting images displayed will tend to be saturated, or too bright.

[0011] By way of further background, the SAA7115 video decoder available from Philips Semiconductors includes different AGC gain adjustment rates depending upon whether the gain is to be increased or decreased. Specifically, the SAA7115 video decoder unconditionally decreases its AGC gain rapidly and increases its AGC gain
5 slowly. It is believed that this differential AGC gain adjustment rate approach is intended to ensure that the gain applied to bright image scenes following dark scenes of brief duration do not saturate.

BRIEF SUMMARY OF THE INVENTION

[0012] It is therefore an object of this invention to provide a video decoder that automatically controls its gain to provide accurate fidelity in the displayed images.

5 [0013] It is a further object of this invention to provide such a video decoder in which automatic gain control (AGC) is not deceived by the clipping of sync level or other portions of the input signal.

[0014] It is a further object of this invention to provide such a video decoder and AGC function in which correction of over-amplified portions of the signal is also performed to further improve the fidelity of the output digital video signals.

10 [0015] It is a further object of this invention to provide such a video decoder and AGC function in which the rate of change in the gain is controlled so that convergence to the true image sequence is optimized.

[0016] Other objects and advantages of this invention will be apparent to those of ordinary skill in the art having reference to the following specification together with
15 its drawings.

[0017] This invention may be implemented into a video decoder integrated circuit in which automatic gain control (AGC) is performed on an incoming video signal based upon multiple attributes of that signal. According to a preferred embodiment of the invention, AGC is applied both prior to and after the separation of the input video
20 signal into its components, where each AGC gain level is controlled using a selected one of multiple attributes of the signal.

[0018] According to another aspect of this invention, increases in the AGC gain are controlled in response to the signal attribute that caused the most recent decrease in AGC gain. More specifically, if the AGC gain is reduced due to an image-dependent
25 attribute of the signal, subsequent increases in the AGC gain are delayed, and then more

slowly increased, relative to the AGC gain increases that are permitted if the most recent AGC gain reduction were due to image-independent attributes of the signal.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING

[0019] Figure 1 is an electrical diagram, in block form, of a display system including a video decoder constructed according to the preferred embodiment of the invention.

5 [0020] Figure 2 is an electrical diagram, in block form, of a video decoder constructed according to the preferred embodiment of the invention.

[0021] Figure 3 is a data flow diagram illustrating the operation of the front-end AGC function in the video decoder constructed according to the preferred embodiment of the invention.

10 [0022] Figure 4 is a flow chart illustrating the operation of the updating of the front-end AGC gain value, according to the preferred embodiment of the invention.

[0023] Figure 5 is a timing diagram illustrating an exemplary input video waveform used by the video decoder according to the preferred embodiment of the invention.

15 [0024] Figure 6 is a data flow diagram illustrating the operation of the back-end AGC function in the video decoder constructed according to the preferred embodiment of the invention.

[0025] Figure 7 is a flow chart illustrating the operation of the updating of the back-end AGC gain value, according to the preferred embodiment of the invention.

20 [0026] Figure 8 is a flow chart illustrating the operation of the control of filter coefficient values for the increasing of AGC gain values, according to the preferred embodiment of the invention.

DETAILED DESCRIPTION OF THE INVENTION

[0027] The present invention will be described in connection with its preferred embodiment, namely as implemented into a video decoder integrated circuit, and a system utilizing that integrated circuit. However, it is contemplated that this invention
5 may have benefit in other applications other than the specific implementation described in this specification. Accordingly, it is to be understood that the following description is provided by way of example only, and is not intended to limit the true scope of this invention as claimed.

[0028] Figure 1 illustrates a video display system into which the preferred
10 embodiment of the invention can be implemented. In this example, multiple video sources 8a through 8n provide video signals, according to one or multiple different standards or formats, to video decoder 10. In turn, video decoder 10 decodes the incoming video signals and presents a digital video signal to video display 12, in a format . Examples of video sources 8a through 8n include conventional video sources
15 such as cable and satellite digital video services, video cameras, video playback devices such as DVD players and video cassette recorders, personal computers, and the like. Examples of the standards or formats of the video signals include composite signals such as those according to the NTSC, PAL, and SECAM standards, high definition TV signals, and digital video signals. Of course, the system may include any number of
20 video sources 8, ranging from a single source to several.

[0029] Video decoder 10 as shown in the system of Figure 1 may be implemented in various parts of the system. For example, video decoder 10 may be implemented within a set-top box, capable of receiving inputs from multiple video sources and delivering digital video signals to display 12. Alternatively, video decoder
25 10 may be physically implemented within one or more of video sources 8 that are capable of providing digital video signals directly to display 12; an example of this implementation is on the graphics card of a personal computer or workstation. Another example is the incorporation of video decoder 10 within a DVD recorder or playback

system, or digital VCR. It is contemplated that these and other arrangements are well-known to those in the art having reference to this specification. For purposes of this invention, the physical location of video decoder 10 is not particularly important.

[0030] Figure 2 illustrates, in block form, the construction of video decoder 10 according to the preferred embodiment of the invention. Input interface 14 receives one or more input video signals from video sources 8a through 8n (Figure 1), and buffers and level-converts those input signals in the conventional manner, according to the nature of the signals received and the requirements of downstream functions within video decoder 10. The input video signals are then applied to analog front-end 15, which performs functions including coarse gain control, analog-to-digital conversion, and also digital automatic gain control according to this preferred embodiment of the invention as will be described below.

[0031] More specifically, analog front-end 15 includes coarse gain control functions 16a, 16b, which apply a coarse AGC correction to the signals received by interface 14. Offset compensation (not shown) may also be applied at this point. As will be described in further detail below, coarse gain control functions 16 operate in the analog domain, yet are digitally controlled. The analog output of coarse gain control functions 16a, 16b are then applied to analog-to-digital converters (ADCs) 18a, 18b, respectively, which sample the amplified analog signals and convert them into the digital domain, as known in the art. Preferably, ADCs 18 are at least of ten-bit resolution, for high-fidelity and high-definition video display functionality. The video signals, now in the digital domain in the form of sampled datastreams, are then applied to respective digital front-end AGC functions 20a, 20b, which apply a finer AGC correction to the digital video signals. According to the preferred embodiment of the invention, digital front-end AGC functions 20a, 20b set their gains by way of feedback measurements of a selected signal attribute, as will be described in further detail below. Offset compensation (not shown) may also be performed after digital front-end AGC functions 20. Input format circuit 24 comprehends the blanking intervals and other synchronization information contained within the video signals, for the particular signal

standard or format received from digital front-end AGC functions 20a, 20b, and strips the displayable information (e.g., luma and chroma) from the incoming video signals, in the conventional manner.

[0032] Separation and filtering function 26 performs the bulk of the video decoding operation, more specifically the luma and chroma separation function. In the case of conventional composite video signals, for example according to the NTSC (National Television System Committee) standard, the luma signal (Y) is a wideband signal that is representative of the overall brightness at each corresponding location of the image to be displayed, and as such is the only signal required for display of a monochrome image. The chroma signal is, according to this standard, communicated by two quadrature-phase signal components that are modulated onto a subcarrier at about 3.6 MHz, each component corresponding to a "color difference". In the NTSC standard, the in-phase component is B-Y (blue minus luma) and the quadrature-phase component is R-Y (red minus luma). Separation and filtering function 26 isolates and separates the luma and chroma signal components from the composite signal. Separation and filtering function 26 also applies the desired filtering to the separated signals, for example by way of an adaptive three-line or four-line comb filter or the like, to reduce cross-luma and cross-chroma artifacts, as known in the art.

[0033] According to the preferred embodiment of the invention, back-end AGC function 28 applies another digital automatic gain control (AGC) to the separated component signals produced by separation and filtering function 26. The manner in which back-end AGC function 28 performs this function will be described in further detail below. The modified signal from back-end AGC function 28 is forwarded to output formatter 30, which demodulates the chroma components and, by way of conventional linear combination or the like, separates the chroma signals into the appropriate color component signals (e.g., Pb, Pr). Output formatter 30 also arranges the resulting digital component values into the appropriate components of the desired format for output to display 12. Examples of typical digital video output formats applied by output formatter 30 include the 4:2:2 format at various data rates, and also

the format specified by the ITU-R.BT.656 standard, with embedded syncs, at various bit rates.

[0034] Video decoder 10 includes support circuitry and functions for controlling its operation, and for providing access to external devices, for example to program the various options under which video decoder 10 is to process the video signals. Clock circuitry 32 receives or generates a system clock signal, and typically includes phase-locked loops for generating internal clock signals for operation of the various functions within video decoder 10. Digital processor 35 is preferably a programmable logic device, for example a microprocessor or digital signal processor (DSP), for performing the necessary digital logic and control operations involved in video decoder 10, including the control of front-end AGC function 20 and back-end AGC function 28. Digital processor 35 is bidirectionally coupled to host interface 36, by way of which control signals are received, and monitoring and status signals are output, by video decoder 10. For example, it is contemplated that configuration commands can be applied to video decoder 10 via host interface 36 to set the operational environment of video decoder 10, including various parameters related to the updating of AGC gain values for front-end AGC function 20 and back-end AGC function 28, as will be described in further detail below.

[0035] Other processing functions (not shown in Figure 2) may also be included within video decoder 10. For example, as known in the art, text and other information is often encoded within the vertical blanking interval (VBI) of a television signal. Examples of this information includes closed captioning or other subtitles associated with the content being displayed. This information may be decoded by a VBI processing circuit within video decoder 10, operating in combination with input format circuit 24. Circuitry for processing copy protection information, for example according to the MACROVISION copy protection scheme, may also be included within video decoder 10, also operating in combination with input format circuit 24. These and other alternative functions and implementations are contemplated to be within the capability of those of ordinary skill in the art having reference to this specification.

[0036] Referring now to Figure 3, the operation of front-end AGC function 20 for a single one of the channels processed by video decoder 10, according to the preferred embodiment of the invention, will now be described in detail. As shown in Figure 3, the input video signal (e.g., from input interface 14) is amplified by coarse AGC stage 16 in front-end AGC function 20. The gain ($gain_{16}$) applied by coarse AGC stage 16 follows a conventional linear AGC characteristic:

$$gain_{16} = a_3 + b_3 N_{CG-1} \quad (1)$$

where N_{CG-1} is a digital control value (e.g., a four-bit value) for setting the coarse gain, and where a_3 is the y-intercept and b_3 is the slope of the linear AGC characteristic. Again, as mentioned above, offset compensation (not shown) may also be performed in the analog domain. ADC 18 converts the output of coarse AGC gain stage 16 to the digital domain.

[0037] Digital front-end AGC stage 20 similarly applies AGC to the digital signal. According to this embodiment of the invention, the gain value $gain_{20}$ of digital front-end AGC stage 20 similarly follows a linear AGC control characteristic:

$$gain_{20} = a_1 + b_1 G_{n-1} \quad (2)$$

where G_{n-1} is a digital control value (e.g., of eight bits or more, for precision) for setting the AGC gain, and where a_1 and b_1 are the y-intercept and slope, respectively, of the linear AGC control function. In one exemplary implementation of this invention, the product of the gain applied by front-end AGC stage 20 with the previously-applied coarse AGC gain may vary in a range from 0.25 to 4.00, each stage applying a gain ranging from 0.50 to 2.00. The amplified input video signal is then clipped, in the conventional manner, by clip stage 42, and is then forwarded along the data path of video decoder 10. As shown in Figure 2, the amplified and clipped input signal is converted into the digital domain by one or both of ADCs 22, and processed according to its input format by input format function 24. Ultimately, the input signal (now in the digital domain) is then processed by separation and filtering function 26, by way of which the luma and chroma signals are separated, and

eventually demodulated and converted into the digital component values as described above.

[0038] According to the preferred embodiment of the invention, front-end AGC stage 20 adjusts its current gain control value G_{n-1} to provide an updated gain control value G_n based on the measurements of multiple attributes in the video signal. As shown in Figure 3, these measurements include measurements of the input signal after gain stage 40 and clip function 42, prior to luma and chroma separation. Some of these attributes may be considered as indirectly correlating to the output signal, while others directly correlate to the output. These measurements of the various attributes of the signal are applied to gain update calculator 46, by way of which the next gain control value G_n is derived. In addition, certain parameters regarding the front-end AGC gain are communicated to back-end AGC function 28, as shown in Figure 3. The particular circuit function within analog front-end 15 that makes these measurements and update calculations may be implemented by custom logic, or by programmable logic under the control of a software routine, or alternatively these measurements and calculations may be made by other circuitry in video decoder 10, for example digital processor 35 (Figure 2) and forwarded to analog front-end 15 as a control signal. While the following description will refer to functions within analog front-end 15 as performing gain update and storage functions, those skilled in the art having reference to this specification will realize that such functions may be implemented in any one of a number of ways, without departing from this invention.

[0039] The manner in which the invention may be applied to video signals may vary with the types of signals being processed. For example, the processing of component video signals (Y, Pb, Pr) may be carried out by calculating the best gain values for the luma component (Y), and then simply applying the same gain values to the chroma components (Pb, Pr). For S-video and composite signals, the most precise AGC method would separately process gain values for the luma and chroma peaks, in effect performing the AGC control in parallel for these signals. However, it has been found that an effective approach for these signals is to calculate the best gain values for

the luma signal according to the preferred embodiment of the invention as described below, and then calculate the AGC gain values for the chroma signals based only on color burst amplitude. It is contemplated that these, and other, alternative implementations of the preferred embodiment of the invention will be apparent to those skilled in the art having reference to this specification, and that these alternative implementations are within the scope of this invention as claimed.

[0040] Figure 4 is a flow diagram illustrating the operation of analog front-end 15 in updating the current gain control value G_{n-1} to its next iterative value G_n , according to the preferred embodiment of the invention. Coarse AGC stage 16 may also similarly adjust its output (more coarsely, of course) in the conventional manner; however, this preferred embodiment of the invention is primarily based on the fine adjustment and control of AGC gain, and as such will generally assume that the coarse gain value N is essentially constant. In this exemplary implementation, analog front-end function 15 continually updates its gain control value G_n on a frame-by-frame basis, based on the content of the video input signal for the most recently received and processed frame n . The applied gain control value G_0 for the first frame may be an arbitrary value, perhaps in the mid-range of the available gains; the gain control value G_n for later frames will be derived by an iterative adjustment from the previous frame. The flow diagram of Figure 4, in combination with Figure 3, illustrates the operation of this gain value updating for an exemplary iteration of this operation.

[0041] It is contemplated that the updating of AGC gain according to this embodiment of the invention may be effected within the vertical blanking interval after the receipt and processing of each frame, so that the new gain value may be applied to the next incoming video frame. Alternatively, it is contemplated that the updating of the gain value may be performed on either a more frequent or a less frequent interval than after each frame. For example, if sufficient processing capacity is available, the gain updating may be performed on a field-by-field basis, or even after every one or several scan lines, to provide even higher fidelity AGC; conversely, if lesser fidelity can be tolerated, cost can be saved by updating the AGC gain after every two (or more) frames.

It is contemplated that those skilled in the art having reference to this specification will be readily able to select the appropriate updating interval. However, it is also contemplated that updating at the frame rate will be optimum in most modern applications of this invention.

5 [0042] Referring to Figure 4, a frame n of video input signals is received and measured in process 50. Referring back to Figure 3, this frame n has already been amplified by front-end AGC stage 20 using the current gain value G_{n-1} , and has been clipped as necessary by clip function 42 according to the particular clipping levels in place. Various attributes of the amplified and clipped video input signals, over frame n ,
10 are then measured from this received frame, by gain update function 46, in combination with the necessary associated circuitry within video decoder 10, in processes 52. In addition, as shown in Figure 3, one or more attributes of the video input signal for frame n are measured in process 52d at a point, in this example, after luma and chroma separation by separation and filtering function 26 (Figure 2). According to this
15 embodiment of the invention, the multiple attributes of the input video signal that are measured in processes 52 include attributes of the signal that are factors in the brightness of the image and in the quality of the video signal itself. Some of these factors are independent of the image to be displayed, and are contained within the horizontal blanking interval between successive scan lines of video signal. Some of the
20 factors indirectly correlate to the output signal level, while others directly correlate to the output.

[0043] Figure 5 illustrates an exemplary waveform of the horizontal blanking interval in an NTSC television signal. As known in the art, the horizontal blanking interval is a portion of the video signal waveform following the image signal for one
25 scan line, and contains signal portions that are used to synchronize the receiving system from a timing and amplitude standpoint. In this NTSC horizontal blanking interval shown in Figure 5, horizontal sync pulse HSYNC is a low level portion of the horizontal blanking waveform following the image data for a scan line, and has a desired level of -40 IRE units relative to the so-called "blanking level" of 0 IRE units in the NTSC

standard. Horizontal sync pulse HSYNC is preceded by a brief "front porch" pulse FP which is nominally at the blanking level of 0 IRE units. Color burst BURST is contained within the so-called "back porch" BP, and consists of an amplitude modulated continuous sine wave at the chroma signal frequency of 3.579545 MHz, and nominally
5 has an amplitude of ± 20 IRE units. Back porch BP refers to the portion of the horizontal blanking waveform from the rising edge of the horizontal sync pulse HSYNC to the beginning of active video, and includes the color burst BURST.

[0044] In process 52a, the attribute measured is the "sync height" of the video signal. In this example, the sync height attribute refers to the amplitude of the
10 horizontal sync tip level, relative to the so-called "back-porch" level in the horizontal blanking interval. Accordingly, the "sync height" measured in process 52a is the difference between the level of the horizontal sync pulse HSYNC and the back porch level BP. Process 52b measures another image-independent attribute in the horizontal blanking interval, namely the amplitude of color burst BURST. Preferably, the attributes
15 of sync height measured by process 52a and of color burst amplitude measured by process 52b are measured for each horizontal blanking interval and averaged over the frame, to account for variations within the frame.

[0045] Process 52c measures an attribute of the video signal that depends upon the image to be displayed, namely the peak amplitude of the composite signal.
20 According to this embodiment of the invention, this peak amplitude is the highest composite signal amplitude for frame n after clipping by clip function 42. This peak amplitude is preferably measured relative to the level of back porch BP in the horizontal blanking interval (i.e., 0 IRE units), and is preferably the peak level encountered anywhere in the frame. This peak composite amplitude measured in process 52c is also
25 measured prior to separation of the luma and chroma signals.

[0046] The attributes of sync height, burst amplitude, and composite peak amplitude, each indirectly correlate to the eventual output from video decoder 10. Process 52d measures an attribute of the video signal following luma and chroma

separation, namely the peak amplitude of the luma signal (i.e., without the modulated chroma, or color difference signals), within the current frame n . This peak luma amplitude directly correlates to the output video signal from video decoder 10, as the luma is the overall brightness of a picture element, without regard to its color or hue. As
5 mentioned above, it is contemplated that the update process of Figure 4 can be performed in real time, before the vertical blanking interval, which permits the measurement of process 52d after separation.

[0047] Those skilled in the art having reference to this specification will appreciate that other signal attributes may additionally be measured, or measured
10 instead of those described above, in connection with the AGC function according to this invention. It is believed, however, that the measurements of processes 52a through 52d will provide a relatively accurate indication of the amplitudes of the video signal for a given frame, for purposes of performing automatic gain control. In addition, measurement processes 52 (and also the corresponding comparison processes 54 to be
15 described below) are shown in Figure 4 as being performed in parallel with one another. This depiction is intended to express that the order in which these measurements and comparisons are made is not important; indeed, these measurements may be made in parallel with one another as shown, sequentially with one another in any order, or in some combination. It is contemplated that those skilled in the art having reference to
20 this specification will be capable of making the appropriate measurements of the video signal for the AGC function in an optimal manner, for example as valid information becomes available, for each given implementation of this invention.

[0048] In process 54a, gain update function 46 compares the sync height measured in process 52a against a desired level. According to this embodiment of the
25 invention, the result of the comparison of process 54a is a ratio of a nominal, or target, sync height (e.g., 40 IRE units) to the measured (averaged) sync height, as such a ratio is useful in the updating of the AGC gain level as will be described below. Similarly, in process 54b, gain update function 46 compares the burst amplitude measured in process 52a against a target amplitude, preferably producing a ratio of the target amplitude to

the measured (averaged) burst amplitude. However, in the event that video decoder 10 cannot lock to a valid color burst, comparison process 54b preferably returns a null value so that the burst amplitude attribute will not be used to set the AGC gain value. Process 54b may not be performed for SECAM input video signals, but is preferably performed for NTSC and PAL input video signals.

[0049] Gain update function 46 compares the peak composite signal amplitude measured in process 52c against a target level to produce a corresponding ratio, in process 54c. Preferably, process 54c is also gated by video decoder 10 locking to a valid color burst; if it is not, comparison process 54c also returns a null value so that the peak composite signal amplitude attribute will not be used to set the AGC gain value.

[0050] In process 54d, gain update function 46 compares the peak luma amplitude for frame n as measured in process 52d against its target amplitude, producing a ratio of the two. This attribute is valid, for purposes of AGC gain setting, for both monochrome and color input signals.

[0051] According to this embodiment of the invention, it is preferred that front-end AGC stage 20 not apply a gain that causes any portion of the video waveform to be clipped. Accordingly, gain update function 46 in this preferred embodiment of the invention updates the gain level G_{n-1} to value G_n by selecting, in process 56, the most conservative one of the comparison ratios derived in processes 54, which will provide the lowest gain. The selected comparison ratio (i.e., the selected attribute and its measured and target values) is also stored in memory 48, in a manner accessible to back-end AGC function 28 as will be described below.

[0052] The comparison ratio selected in process 56 is then applied to a gain update equation by gain update function 54, in process 58. According to the preferred embodiment of the invention, the gain update equation is a recursive equation, by way of which the current gain value G_{n-1} is modified by an error value, typically the error value multiplied by a filter (or convergence) factor, to derive the next gain value G_n . The

filter factor is also referred to as a time constant of the recursive updating equation. The gain update equation may be considered as a "fine control" on the AGC gain. Adjustment of the gain of coarse AGC gain stage 16 may be accomplished by gain update function 46, for example by incrementing the coarse gain if the fine gain value
5 (G_n) overflows, and decrementing the coarse gain is incremented if the fine gain value underflows.. As mentioned above, the coarse AGC gain applied by stage 16 is an analog AGC gain, controlled in the conventional manner, while the fine control gain applied by front-end AGC function 20 is applied in the digital domain.

[0053] Many variations on AGC gain equations are known in the art. In general,
10 according to this preferred embodiment of the invention, an updated gain value $gain(n)$, based on the previous gain value $gain(n-1)$, can be expressed as:

$$gain(n) = (1 - \beta)gain(n - 1) + \beta(des_gain) \quad (3)$$

where the value des_gain is a desired gain that is determined from a measurement of the signal. For the case of feedback control, in which the signal is measured after the
15 application of gain $gain(n-1)$ that is being adjusted, value des_gain amounts to a desired multiplier of the applied gain. For example, if the measured signal is one-half the desired amplitude, then the value des_gain will correspond to doubling of the applied gain. Conversely, in the feed-forward context (as will be described below), where the signal is measured prior to the application of gain $gain(n-1)$ to be adjusted, the value
20 des_gain amounts to an absolute measure; for example, if the measured signal is one-half the desired amplitude, the value des_gain will correspond to an absolute gain of two.

[0054] In the case of front-end AGC gain stage 20, gain value $gain_{20}$ is adjusted by feedback control, based on measurement of the signal after front-end AGC gain stage 20 (and clip function 42). As noted above, adjustment of gain value $gain_{20}$ is effected by
25 determining an adjusted digital AGC gain control value G_n which, according to equation (2), will produce an adjusted gain $gain_{20}$. According to this preferred embodiment of the

invention, the adjusted digital control value G_n is a linear function of the amplitude reference on which the gain is based is:

$$G_n = -\frac{a_1}{b_1} + (1 - \beta)\left(\frac{a_1}{b_1} + G_{n-1}\right) + \beta\left(\frac{a_1}{b_1} + G_{n-1}\right)\left(\frac{\Delta N_t}{\Delta N_m}\right) \quad (4)$$

where β is a filter coefficient value serving as a filter factor (ranging between 0 and 1),
 5 ΔN_t is the target, or desired, value of the selected amplitude reference (i.e., of the signal attribute selected in process 56) and ΔN_m is the measured value of the selected amplitude reference (i.e., the value measured in process 52 for the attribute selected in process 56). As mentioned above, the value a_1 is the y-intercept of the gain control equation (2), and the value b_1 is the slope of the gain control equation (2). For ease of computation, the y-
 10 intercept value a_1 of this gain control equation (2) may be set to zero, in which case gain update equation (3) simplifies to:

$$G_n = (1 - \beta)(G_{n-1}) + \beta G_{n-1} \left(\frac{\Delta N_t}{\Delta N_m} \right) \quad (5)$$

or more simply yet:

$$G_n = G_{n-1} + \beta G_{n-1} \left(\frac{\Delta N_t}{\Delta N_m} - 1 \right) \quad (6)$$

15 As especially evident from gain update equation (5), a larger value of filter coefficient value β (i.e., a value closer to 1) permits the target-to-attribute ratio to more strongly affect the updated digital control value G_n , while a smaller value of filter coefficient value β (i.e., a value closer to 0) permits the target-to-attribute ratio to more weakly affect the updated digital control value G_n .

20 [0055] The particular gain update equation used in process 58 preferably follows a linear update equation, for example as generally described in equations (4) through (6) above. Other relationships, including higher order gain update equations, may

alternatively be used in process 58 if desired. The frame index value n is incremented, in preparation for the next frame of incoming video signals, and the process is repeated for this next frame beginning again with process 50.

[0056] As discussed above relative to Figure 2, back-end AGC function 28
5 amplifies the separated signals to apply another gain value, in an automatic gain control (AGC) manner. According to this preferred embodiment of the invention, back-end AGC function 28 applies its gain $gain_{28}$ according to a linear gain control equation:

$$gain_{28} = a_2 + b_2 S_{n-1} \quad (7)$$

10 in which a_2 and b_2 are the y-intercept and slope, respectively, and in which S_{n-1} is the current digital control value. Also according to this embodiment of the invention, the back-end AGC gain is updated according to a selected one of multiple signal attributes, so that the correction applied by back-end AGC function 28 also avoids miscorrection of the image amplitude, particularly to avoid saturation. The gain applied by back-end AGC function 28 thus further ensures that the output of video decoder 10 is not improperly compensated.

15 [0057] As shown in Figure 6, back-end AGC function 28 applies a gain value S_{n-1} to the luma signal Y SIGNAL and the color difference signals C SIGNAL after separation by separation and filtering function 26 (Figure 2), using gain stages 62Y, 62C, respectively. In one exemplary implementation of this invention, this back-end AGC gain value S_{n-1} may vary in a range from 1.00 to 2.00. Gain stages 62Y, 62C thus apply
20 AGC gain amplification to the separated luma and color difference signals, to further correct and compensate for artifacts in the video decoding process and further improve the fidelity of the displayed image.

[0058] Back-end AGC function 28 also includes gain update function 66, which receives the results of the measurements carried out in processes 52 in connection with
25 front-end AGC function 20, described above. These measurements include the peak luma amplitude in signal Y SIGNAL, taken prior to gain stage 62Y as shown in Figure 6. Gain update function 66 calculates an updated gain control value S_n based upon these

measurements, following the general approach described above relative to equation (3). As will become apparent from the following description, the updating of the control value S_n is performed in a feed-forward manner. The process for updating back-end AGC gain control value S_n according to the preferred embodiment of the invention will now be described in detail in connection with Figure 7.

[0059] As in the updating of the front-end AGC gain control value G_n described above, multiple attributes of the video signal are also used to update the back-end AGC gain control value S_n . In process 72a, gain update function 66 retrieves the comparison ratio for the attribute selected in process 56 from store 48 (Figure 3), including both an identification of which attribute the comparison ratio is associated with, and also the value of the selected comparison ratio itself. In process 74b, gain update function 66 retrieves the color burst amplitude comparison ratio derived in process 54b for frame n . In process 74c, gain update function 66 retrieves the comparison ratio of the luma peak amplitude for frame n as derived in process 54d, and in process 74d, gain update function 66 retrieves the comparison ratio for the sync height amplitude as derived in process 54a for frame n . According to the preferred embodiment of the invention, the comparison ratio for the measured composite peak amplitude (prior to signal separation) is not used for updating the back-end AGC gain.

[0060] The comparison ratio used in the front-end AGC gain update calculation for frame n is interrogated in decision 73. If the front-end AGC gain calculation used the luma peak to update its gain (decision 73 is YES), the back-end AGC gain update calculation sets the updated back-end AGC gain control value S_n to a default value of unity, in process 76. In other words, if the front-end AGC gain compensated for the separated luma signal already, the back-end AGC gain will not further compensate the output signal. If the luma peak was not the attribute used in the front-end AGC gain update (decision 73 is NO), control passes to attribute selection process 74.

[0061] In process 74, gain update function 66 selects the signal attribute that has the smallest comparison ratio (target to measured). As before, so that back-end AGC

function 28 does not saturate the signal, the back-end AGC gain value is adjusted according to the attribute requiring the least amount of gain. Selection process 74 excludes, from its selection, the attribute that was used in the front-end AGC gain update process, as determined from process 72a. In process 78, the current back-end AGC gain value S_{n-1} is modified according to a gain update equation, as will now be described in detail.

[0062] In its most general sense, the back-end AGC gain update equation involves parameters in addition to those involved in the front-end AGC gain equation. This is because the back-end AGC gain update must account for updates to the coarse AGC gain stage 16 (if any) and updates to the front-end AGC gain stage 20. In other words, the next back-end AGC gain value $gain_{28}(n)$ will be applied to a frame of video signals to which updated gain values $gain_{14}(n)$ and $gain_{20}(n)$ will also have been applied. Accordingly, the back-end AGC gain update equation will depend upon the current and updated front-end AGC gain control values G_{n-1} , G_n , respectively, and also current and updated values of the coarse analog gain control values N_{CG-1} , N_{CG} , respectively. An generalized expression of the back-end AGC gain update equation according to the preferred embodiment of the invention is:

$$S_n = -\frac{a_2}{b_2} + (1 - \beta) \left(\frac{a_2}{b_2} + S_{n-1} \right) + \beta \left(\frac{1}{b_2} \right) \left(\frac{\Delta N_t}{\Delta N_m} \right) \left(\frac{\frac{a_3}{b_3} + N_{CG-1}}{\frac{a_3}{b_3} + N_{CG}} \right) \left(\frac{\frac{a_1}{b_1} + G_{n-1}}{\frac{a_1}{b_1} + G_n} \right) \quad (8)$$

The values ΔN_t and ΔN_m are the target and measured values, respectively, of the amplitude reference (i.e., signal attribute) selected in process 74. As before, the value β is a filter coefficient that controls the rate at which the back-end gain control value changes from iteration to iteration. In a more typical case, the y-intercept values a_1 and a_2 are zero, and equation (4) simplifies to:

$$S_n = S_{n-1} + \beta \left[\left(\frac{1}{b_2} \right) \left(\frac{\Delta N_t}{\Delta N_m} \right) \left(\frac{G_n}{G_{n-1}} \right) \left(\frac{\frac{a_3}{b_3} + N_{CG-1}}{\frac{a_3}{b_3} + N_{CG}} \right) - S_{n-1} \right] \quad (9)$$

Back-end gain update function 66 thus performs, in process 78, the required calculations to generate updated back-end AGC gain value S_n , using the appropriate one of equations (8) or (9), or a variation thereof depending upon the particular gain equations to be used. This gain value S_n is then forwarded to gain stages 62Y, 62C for use in connection with the next frame to be processed, which will be referred to by the frame index n that is incremented in process 80. The back-end gain update process is then repeated, from processes 72, upon receipt and measurement of that next frame.

[0063] According to the preferred embodiment of the invention, additional performance improvement may be made available by an optional feature in which the rate of change of the front-end and back-end AGC gain control values, and thus the AGC gains themselves, are controlled to more closely correspond to image-dependent factors in the video signal. In the operation of video decoder 10 described above, the image-dependent signal attributes of the composite peak amplitude and the luma peak amplitude control the AGC gain only when both the sync height and color burst amplitudes are below their target levels. In this case, peak values in a bright image scene will drive the AGC gain down, and the sync height or burst amplitude attributes will drive the AGC gain back up during dark image scenes. However, if another bright scene appears after the AGC gain has been driven high by the non-image-dependent attributes, the output signal will saturate until the image-dependent attributes again drive the AGC gain down. This effect causes the display of bright image scenes after dark scenes to be unduly brightened by the AGC gain values, resulting in poor fidelity output.

[0064] According to the preferred embodiment of the invention, therefore, the filter coefficient values β for the adjustment of both the front-end and back-end AGC

gain update equations are controlled to delay and slow AGC gain increases, following a decrease in the AGC gain caused by image dependent attributes. The filter coefficient values β for the front-end and back-end AGC gain update equations may be the same, or may differ, depending upon the particular implementation. In any case, the general operation of gain update functions 46, 66 to control the rate and delay of gain increases and decreases is similar to one another, and will now be described generally with reference to Figure 8. This process preferably operates in conjunction with the gain update processes 58, 78 described above, and may be performed by circuitry implemented within AGC functions 20, 28, or alternatively elsewhere within video decoder 10, such as digital processor 35. Specifically, it is contemplated that the particular parameters of the process of Figure 8, such the various filter coefficient values β and frame delay values, are programmable by way of host interface 36 to digital processor 35, as shown in Figure 2.

[0065] The process of Figure 8 begins with the receipt of a frame n of video signal data. Decision 81 determines whether the AGC gain is to be increased or decreased based on the selected signal attribute and its comparison ratio. If the gain is to be decreased (decision 81 is NO), a frame counter is reset to zero in process 82, and the identity of the signal attribute causing this decrease is stored in memory by process 84. According to the preferred embodiment of this invention, all AGC gain decreases occur immediately, and at relatively fast rates. Accordingly, a high filter coefficient value β is selected in process 86, and is used in the corresponding AGC gain update process 58, 78. An example of a high filter coefficient value β for front-end AGC gain update process 58 is $1/8$, and is unity for back-end AGC gain update process 78. Control then returns to decision 81 to await the next frame of the incoming video signal.

[0066] If the AGC gain is to be increased (decision 81 is YES), process 88 is then performed to retrieve, from memory, the identity of the signal attribute that caused the most recent AGC gain decrease. If the retrieved signal attribute causing the last decrease is an image independent attribute (e.g., sync height or color burst amplitude),

decision 81 returns a NO result, indicating that the gain increase can occur immediately and rapidly. Process 92 is then executed to select a high filter coefficient value β , for example similar to the filter coefficient values for AGC gain decreases, and the corresponding AGC gain update process 58, 78 is then performed. Control then passes
5 to decision 81 to again await the next frame of the incoming video signal.

[0067] If, on the other hand, the attribute causing the most recent AGC gain decrease is an image dependent attribute (e.g., composite signal peak amplitude, or luma signal peak amplitude), decision 89 returns a YES result. The frame counter is then incremented, in process 92, and its contents compared against a delay limit value in
10 decision 93. The delay limit value is contemplated to be a programmable value indicating the number of frames for which indicated gain increases are to be delayed, and may vary, for example, from zero to 255, with a typical value of about 30 frames. If the number of gain increase frames counted by the frame counter has not yet reached the delay limit value (decision 93 is NO), then no AGC gain update is performed, and
15 control returns to decision 81 to await the next frame of incoming video signals.

[0068] If the delay limit value is reached by the frame counter (decision 93 is YES), process 94 is executed to select a low filter coefficient value β for use in the appropriate AGC gain update process 58, 78. Examples of low filter coefficient values β range from unity to $1/128$, with a typical value being on the order of $1/64$. Obviously,
20 the low high filter coefficient value β will be lower than the high filter coefficient value β used for gain decreases, and gain increases following non-image-dependent gain decreases. Following selection process 94, the corresponding AGC gain update process 58, 78 is performed, and control returns to decision 81 to await the next frame of incoming video signals.

25 [0069] According to this preferred embodiment of the invention, therefore, the rate at which the AGC gain is increased depends upon the reason for which the AGC gain was most recently decreased. If the decrease in AGC gain was due to image-dependent signal attributes, successive AGC gain increases are delayed and slowed.

This improves the fidelity of the video image sequences, by reducing the likelihood of image saturation due to bright images following a short sequence of relatively dark images in the signal. The resulting overall video result is therefore improved over that provided by conventional video decoders.

5 [0070] According to this preferred embodiment of the invention, therefore, the use of multiple attributes of the incoming video signal provides improved fidelity in the output video signal that is forwarded to the eventual video display. Over-correction, for example in the form of over amplifying the signal resulting in saturated "white" peaks in the displayed image, is avoided to a larger extent. In addition, the application of both
10 front-end (pre-separation) and back-end (post-separation) AGC functions, both of which have their gains updated according to a selected one of the multiple signal attributes, provides improved overall gain control and thus improved fidelity in the displayed images. Also according to the preferred embodiment of the invention, both image-dependent and image-independent attributes are measured and may be used for gain
15 control; these attributes also include those attributes that directly correlate to the output video signal, and those that only indirectly correlate to the output. And, as mentioned above, the rate and delay of gain increases can be controlled according to the cause of the most recent gain decrease, further preventing the likelihood of image saturation at the display. This invention thus provides a robust and high-fidelity approach to
20 automatic gain control in the video decoder context.

[0071] While the present invention has been described according to its preferred embodiments, it is of course contemplated that modifications of, and alternatives to, these embodiments, such modifications and alternatives obtaining the advantages and benefits of this invention, will be apparent to those of ordinary skill in the art having
25 reference to this specification and its drawings. It is contemplated that such modifications and alternatives are within the scope of this invention as subsequently claimed herein.